L Number	Hits	Search Text	DB	Time stamp
-	134	align$3 near5 bits near5 bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:13
-	15646	configur$5 near5 register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 09:19
-	0	(align$3 near5 bits near5 bus) same (configur$5 near5 register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 09:19
-	2994	align$3 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 09:19
-	17	(configur$5 near5 register) same (align$3 near5 bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 09:32
-	32	(align$3 near5 bits near5 bus) same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 09:33
-	11	(align$3 near5 bits near5 bus) same shift$3 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 09:55
-	39	(haycock near3 matthew).inv.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:02
-	58	(bryan near3 casper).inv.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:02
-	33	((bryan near3 casper).inv.) and intel.asn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:08
-	369	713/503.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:08
-	1	713/503.ccls. and (align$3 near5 bits near5 bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:09
-	10	713/503.ccls. and (align$3 near5 bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:22

-	2	5832047.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:59
-	3	align$3 near5 bits near5 bus same pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:59
-	219	$3align$3 near5 bits same pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:00
-	114	$3align$3 near5 bits near8 pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:00
-	1	$3align$3 near5 bits near5 bus near8 pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:00
-	3	$3align$3 near5 bits near5 bus same pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:10
-	10	$3align$3 near5 bits same test near3 pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:11
-	639	$3align$4 near5 bit$1 near8 register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/02 10:52
-	60	($3align$4 near5 bit$1 near8 register) same pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/02 10:11
-	6	realign$4 near5 bit$1 near8 register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/02 10:53
-	200	realign$4 near5 bit$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/02 10:53
-	27	(realign$4 near5 bit$1) same pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/02 10:54
-	108	708/212.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:36
-	5	708/212.ccls. and $3align$4 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:38

-	1427	710/52.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:42
-	20	710/52.ccls. and $3align$4 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:42
-	104	710/55.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:42
-	1	710/55.ccls. and $3align$4 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:42
-	153	misalign$4 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:39
-	6	correct$3 near5 misalign$4 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:48
-	1	adjust$3 near5 misalign$4 near5 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:48
-	15	(misalign$4 near5 bits) same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:34
-	3	(misalign$4 near5 bits) same realign$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:34
-	3	misalign$4 near5 bits near8 parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:39
-	847	configur$5 near5 parallel same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 12:56
-	161	configur$5 near5 parallel near3 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 12:56
-	44	configur$5 near5 parallel near3 bits near8 register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:00
-	38	configur$5 near5 register near8 parallel near3 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:19

-	104	configur$5 near5 register near8 $3align$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:20
-	2	configur$5 near5 register near8 $3align$4 near3 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:21
-	4	configur$5 near5 register near8 $3align$4 near3 bit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:22
-	2	configur$5 near5 register near8 misalign$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:24
-	18	configur$4 near5 parallel adj3 registers	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:39
-	21	configur$4 near8 parallel adj3 registers	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:40
-	172	bus near8 parallel adj3 registers	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:40
-	0	$3align$4 near5 bus near8 parallel adj3 registers	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 13:40
-	10	$3align$4 near8 parallel adj3 registers	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:02
-	182	parallel adj3 registers near8 bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:03
-	1	(parallel adj3 registers near8 bus) near8 figure	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:03
-	9	(parallel adj3 registers near8 bus) near8 bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:16
-	2	compar$4 near3 bits near5 registers near5 parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:20
-	323	compar$4 near3 bits near5 registers	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:24

-	66	(compar$4 near3 bits near5 registers) same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:21
-	23	compar$4 near3 bits near5 registers near8 bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:22
-	3	(compar$4 near3 bits near5 registers) same $3align$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:22
-	278	compar$4 near3 bits same $3align$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:24
-	62	compar$4 near3 bits near8 $3align$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 14:24
-	13	(compar$4 near3 bits near8 $3align$4) same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:10
-	220	selective$2 near5 conduct$3 near5 cells	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:11
-	2	selective$2 near5 conduct$3 near5 register near3 cells	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:11
-	1	selective$2 near5 conduct$3 near5 cells same $3align$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:14
-	0	selective$2 near5 conduct$3 near5 register$1 near3 cell	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:14
-	2	selective$2 near5 conduct$3 near5 register$1 near3 cell$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:14
-	65	selective$2 near5 conduct$3 near5 register$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/05 15:15
-	51	shift adj3 register same select$3 near5 register adj3 cell	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:15
-	19	register adj3 cells same maximum near5 number	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:19

-		1	register adj3 cells same maximum near5 $3align$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:19
-		1101	"2m-1"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:19
-		2	"2m-1" same register near5 cells	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:20
-		52	"2m-1" same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:20
-		2	6753872.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:44
-		6	circuit adj3 boards same slots same parallel adj3 bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:45